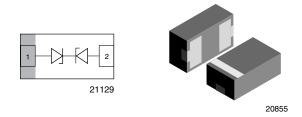


**Vishay Semiconductors** 

## Bidirectional Symmetrical (BiSy) Single Line ESD-Protection Diode in LLP1006-2L



**MARKING** (example only)



Bar = pin 1marking

X = date code

SHA

Y = type code (see table below)

#### FEATURES

- Ultra compact LLP1006-2L package
- Low package profile < 0.4 mm</li>
- 1-line ESD-protection
- Working range ± 5 V
- Low leakage current I<sub>R</sub> < 0.1 μA</li>
- Low load capacitance C<sub>D</sub> = 18 pF
- ESD-protection acc. IEC 61000-4-2

± 20 kV contact discharge



RoHS

COMPLIANT

<u>GREEN</u> (5-2008)\*\*

± 25 kV air discharge
Soldering can be checked by standard vision inspection; no X-ray necessary

- Pin plating NiPdAu (e4) no whisker growth
- e4 precious metal (e.g. Ag, Au, NiPd, NiPdAu) (no Sn)
- Compliant to RoHS Directive 2002/95/EC and in accordance to WEEE 2002/96/EC

ORDERING INFORMATION				
DEVICE NAME ORDERING CODE		TAPED UNITS PER REEL (8 mm TAPE on 7" REEL)	MINIMUM ORDER QUANTITY	
VCUT0505B-HD1	VCUT0505B-HD1-GS08	8000	8000	

PACKAGE DATA						
DEVICE NAME	PACKAGE NAME	TYPE CODE	WEIGHT	MOLDING COMPOUND FLAMMABILITY RATING	MOISTURE SENSITIVITY LEVEL	SOLDERING CONDITIONS
VCUT0505B-HD1	LLP1006-2L	L	0.72 mg	UL 94 V-0	MSL level 1 (according J-STD-020)	260 °C/10 s at terminals

ABSOLUTE MAXIMUM RATINGS						
PARAMETER	TEST CONDITIONS	SYMBOL	VALUE	UNIT		
Peak pulse current	acc. IEC 61000-4-5; $t_p = 8/20 \ \mu s$ ; single shot	I <sub>PPM</sub>	3.5	А		
Peak pulse power	Pin 1 to pin 2 acc. IEC 61000-4-5; $t_p = 8/20 \ \mu s$ ; single shot	P <sub>PP</sub>	56	W		
	Contact discharge acc. IEC 61000-4-2; 10 pulses	M	± 20	kV		
ESD immunity	Air discharge acc. IEC 61000-4-2; 10 pulses	V <sub>ESD</sub>	± 25	kV		
Operating temperature	Junction temperature	TJ	- 40 to + 125	°C		
Storage temperature		T <sub>stg</sub>	- 55 to + 150	°C		

<b>ELECTRICAL CHARACTERISTICS</b> (T <sub>amb</sub> = 25 °C, unless otherwise specified)						
PARAMETER	TEST CONDITIONS/REMARKS	SYMBOL	MIN.	TYP.	MAX.	UNIT
Protection paths	Number of lines which can be protected	N <sub>lines</sub>	-	-	1	lines
Reverse working voltage	at I <sub>R</sub> = 0.1 μA	V <sub>RWM</sub>	5	-	-	V
Reverse current	at V <sub>R</sub> = 5 V	I <sub>R</sub>	-	-	0.1	μΑ
Reverse breakdown voltage	at I <sub>R</sub> = 1 mA	V <sub>BR</sub>	7	-	-	V
Reverse clamping voltage	at I <sub>PP</sub> = 1 A	V <sub>C</sub>	-	-	12	V
	at $I_{PP} = I_{PPM} = 3.5 \text{ A}$	V <sub>C</sub>	-	-	16	V
Capacitance	at $V_R = 0 V$ ; f = 1 MHz	CD	-	18	20	V
	at V <sub>R</sub> = 2.5 V; f = 1 MHz	CD	-	14.5	-	pF

\*\* Please see document "Vishay Material Category Policy": <u>www.vishay.com/doc?99902</u>



## metrical (BiSy) Sing



#### Vishay Semiconductors Bidirectional Symmetrical (BiSy) Single Line ESD-Protection Diode in LLP1006-2L

### CUT THE SPIKES WITH VCUT0505B-HD1:

The VCUT0505B-HD1 is a Bidirectional and Symmetrical (BiSy) ESD-protection device which clamps positive and negative overvoltage transients to ground. Connected between the signal or data line and the ground the VCUT0505B-HD1 offers a high isolation (low leakage current, low capacitance) within the specified working range. Due to the short leads and small package size of the tiny LLP1006-2L package the line inductance is very low, so that fast transients like an ESD-strike can be clamped with minimal over- or undershoots.

#### TYPICAL CHARACTERISTICS (Tamb = 25 °C, unless otherwise specified)

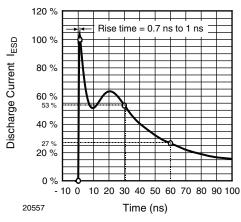


Fig. 1 - ESD Discharge Current Wave Form acc. IEC 61000-4-2 (330  $\Omega/150$  pF)

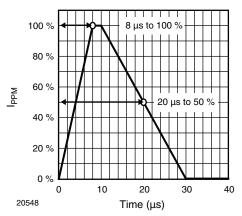


Fig. 2 - 8/20 µs Peak Pulse Current Wave Form acc. IEC 61000-4-5

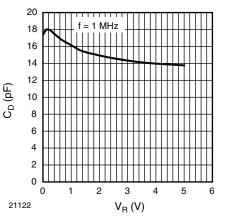


Fig. 3 - Typical Capacitance  $C_{\text{D}}$  vs. Reverse Voltage  $V_{\text{R}}$ 

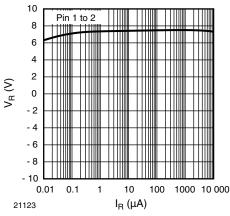


Fig. 4 - Typical Reverse Voltage  $V_R$  vs. Reverse Current  $I_R$ 



## VCUT0505B-HD1

## Bidirectional Symmetrical (BiSy) Single Line Visha ESD-Protection Diode in LLP1006-2L

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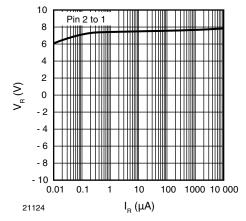


Fig. 5 - Typical Reverse Voltage  $V_{\mathsf{R}}$  vs. Reverse Current  $I_{\mathsf{R}}$ 

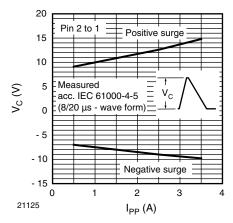


Fig. 6 - Typical Peak Clamping Voltage  $V_C$  vs. Peak Pulse Current I<sub>PP</sub>

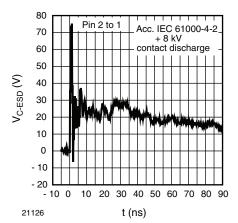


Fig. 7 - Typical Clamping Performance at + 8 kV Contact Discharge (acc. IEC 61000-4-2)

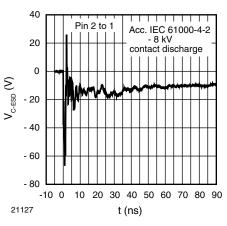


Fig. 8 - Typical Clamping Performance at - 8 kV Contact Discharge (acc. IEC 61000-4-2)

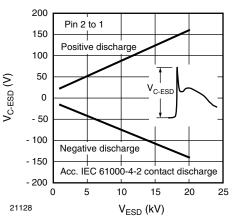


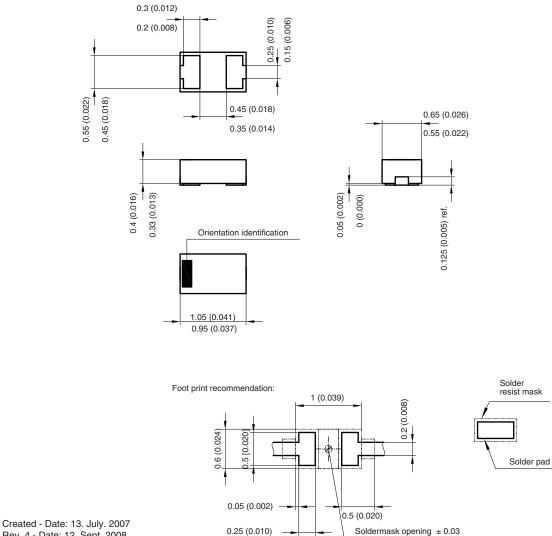
Fig. 9 - Typical Peak. Clamping Voltage at ESD Contact Discharge (acc. IEC 61000-4-2)

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Vishay Semiconductors Bidirectional S

Bidirectional Symmetrical (BiSy) Single Line ESD-Protection Diode in LLP1006-2L

## PACKAGE DIMENSIONS in millimeters (inches): LLP1006-2L



measured middle of the package

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